



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,225	12/14/2001	David Brian Wecker	MSFT-0669/171951.1	4177

7590 04/03/2006

Susan C. Murphy  
Woodcock Washburn LLP  
46th Floor  
One Liberty Place  
Philadelphia, PA 19103

EXAMINER
----------

MIZAN, SHAHIN

ART UNIT	PAPER NUMBER
----------	--------------

2132

DATE MAILED: 04/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/022,225	<b>Applicant(s)</b> WECKER, DAVID BRIAN	
	<b>Examiner</b> Shahin Mizan	<b>Art Unit</b> 2132	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 12 January 2006.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1,5-10 and 12-17 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,5-10 and 12-17 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 December 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |                                                                                                                        |                                                                                         |
|------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)                                            | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____                                                |

## **DETAILED ACTION**

### ***Response to Amendment***

1. Applicant's amendments/arguments with respect to amended claims 1, 5-10, and 12-17 filed on January 12, 2006 have been fully considered (MPEP 714.04; 37 CFR 1.111) but they are not persuasive. Claims 2-4 and 11 are cancelled.

### ***Response to Arguments***

2. Applicant's arguments with respect to claims 1, 5-10, and 12-17 have been considered but are not persuasive.

With regards to Applicant's argument that Candelore et al. does not disclose the generation of a database of records comprising object identifiers and randomly deleting records in the database (*note column 19, lines 36-43 and column 24, line 46-67 – the underlying functionality achieved by the use of the database is disclosed in the Candelore patent i.e. storing the program in a non contiguous memory location. The use of database is a particular implementation of the idea disclosed in the Candelore patent; also note column 2, lines 9-16 – the relevancy of object oriented programming is disclosed*). In addition, with regards to the argument that Candelore et al. does not disclose the generation of a machine identifier from the object identifier of the randomly deleted records, Examiner respectfully disagrees (*note column 15, lines 42-50, column 19, lines 40-43, and column 24, lines 54-67 – the key is the unique identifier which is dependent on randomly selected memory address*).

With regards to Applicant's argument that Candelore et al. does not disclose generating a machine identifier based on locations derived from a list of records

randomly deleted from a database of object identifiers as recited in amended independent claim 8, Examiner respectfully disagrees (*note column 19, lines 36-43 and column 24, lines 46-67 – the underlying functionality achieved by the use of the database is disclosed in the Candelore patent i.e. storing the program in a non contiguous memory location. The use of database is a particular implementation of the idea disclosed in the Candelore patent; also note column 2, lines 9-16 – the relevancy of object oriented programming is disclosed*).

With regards to Applicant's argument that Candelore et al. does not disclose a random number generator that selects dummy records to be deleted, wherein the selected records are deleted, and the locations of the deleted records added to the list of unused locations from which the machine identifier is generated as recited in amended Claim 14 (*note column 19, lines 36-43 and column 24, lines 46-67 – the underlying functionality achieved by the use of the database is disclosed in the Candelore patent i.e. storing the program in a non contiguous memory location. The use of database is a particular implementation of the idea disclosed in the Candelore patent; also note column 2, lines 9-16 – the relevancy of object oriented programming is disclosed; also note column 15, lines 42-50, column 19, lines 40-43, and column 24, lines 54-67 – the key is the unique identifier which is dependent on randomly selected memory address*).

### ***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1, 5-10, and 12-17 are rejected under 35 U.S.C. 102(b) as being anticipated by Candelore et al. (US Patent # 6,061,449).

As per independent claim 1, Candelore et al. teaches a method of generating a machine identifier comprising:

generating a database of records comprising object identifiers, each record having an associated memory block location *(note column 19, lines 36-43 and column 24, lines 46-67 – the underlying functionality achieved by the use of the database is disclosed in the Candelore patent i.e. storing the program in a non contiguous memory location. The use of database is a particular implementation of the idea disclosed in the Candelore patent; also note column 2, lines 9-16 – the relevancy of object oriented programming is disclosed);*

randomly deleting records in the database, the memory locations of the deleted records becoming available for allocation *(note column 19, lines 36-43 and column 24, lines 46-67 – the underlying functionality achieved by the use of the database is disclosed in the Candelore patent i.e. storing the program in a non contiguous memory location. The use of database is a particular implementation of the idea disclosed in the Candelore patent; also note column 2, lines 9-16 – the relevancy of object oriented programming is disclosed);*

allocating a file comprising blocks of the memory locations of the deleted records, each of the blocks having an object identifier based on the block's location in a memory *(note Fig. 1 – a file can be created in the external memory labeled as 110 using randomly located memory blocks; also note column 18, line 47; also note column 19, lines 36-43; also note column 2, line 13 – the invention is applicable to Java language implying object oriented methodology); and*

creating a machine identifier based on the object identifiers of the deleted records associated with the allocated memory block locations *(note column 15, lines 42-50, column 19, lines 40-43, and column 24, lines 54-67 – the key is the unique identifier which is dependent on randomly*

Art Unit: 2132

*selected memory address; also note Fig. 1 and column 19, lines 36-43 – a unique machine identifier can be created using the object ID associated with the memory blocks in the storage device).*

As per claim 5, which is dependent on claim 1, Candelore et al. teaches the method of claim 1, further comprising:

transmitting said machine identifier to a remote computing device which creates a program based on said machine identifier (*note column 18, lines 14-17 – the machine ID can be transmitted via the stated communication links e.g. telephone line or internet*); and

receiving said program from said remote computing device and storing said program in said file, said program being adapted to check that the object identifiers of the memory blocks of which the file is comprised are consistent with said machine identifier (*note column 18, lines 14-17 – the software or program is received into the external memory via the communication link stated e.g. telephone line or internet; also note Fig. 1 – the mechanism for verifying the embedded machine ID dependent code is shown*).

As per claim 6, which is dependent on claim 5, Candelore et al. teaches the method of claim 5, wherein said program operates to decrypt encrypted content when said program is running on a device having a particular machine identifier, and wherein said program denies decryption of said encrypted content if the object identifiers of said memory blocks of which the file is comprised are inconsistent with said machine identifier (*note Fig. 1 – the encryption and decryption mechanism is described; also note column 24, lines 36-45 – various encryption algorithms and scrambling algorithms described*).

As per claim 7, which is dependent on claim 5, Candelore et al. teaches the method of claim 5, wherein said machine identifier comprises a concatenation of the object identifiers of said memory blocks of which the file is comprised, wherein the

Art Unit: 2132

machine identifier is embedded in the program, and wherein the program checks whether the machine identifier is consistent with the object identifiers by concatenating the object identifiers of the file in which the program is stored and comparing the concatenated object identifiers to the embedded machine identifier (*note Fig. 1 – a file can be created in the external memory labeled as 110 using randomly located memory blocks; also note column 18, line 47; also note column 19, lines 36-43; also note column 2, line 13 – the invention is applicable to Java language implying object oriented methodology; also note Fig. 1 and column 19, lines 36-43 – a unique machine identifier can be created using the object ID associated with the memory blocks in the storage device*).

As per independent claim 8, Candelore et al. teaches a computer-readable medium having stored thereon computer-readable instructions that:

allocate a file in memory, said file comprising at least a first block and a second block, said first block being associated with a first randomized value representing a first location in memory where said first block is located and said second block being associated with a second randomized value representing a second location in memory wherein said second block is located (*note Fig. 1 – shows the hardware and associated software to accomplish these functions; note column 24, lines 21-23 – a one block implementation is described; also note column 20, line 30 – multi blocks or chaining concepts is described; also note column 11, lines 6-19*); and

generate a machine identification based on said first value and said second value, wherein the first block and the second block comprise locations derived from a list of records randomly deleted from a database of object identifiers (*note column 15, lines 42-50, column 19, lines 40-43, and column 24, lines 54-67 – the key is the unique identifier which is dependent on randomly selected memory address; also note Fig. 1 and column 19, lines 36-43 – a*

Art Unit: 2132

*unique machine identifier can be created using the object ID associated with the memory blocks in the storage device).*

As per claim 9, which is dependent on claim 8, Candelore et al. teaches the computer-readable medium of claim 8, wherein said machine identification is generated by concatenating at least said first and second values *(note Fig. 1 and column 19, lines 36-43 – a unique machine identifier can be created using the object ID associated with the memory blocks in the storage device).*

As per claim 10, which is dependent on claim 8, Candelore et al. teaches the computer-readable medium of claim 8, having stored thereon further computer-readable instructions that receive a program from a remote computing device and store said program in said file, said program being adapted to check that said machine identification is consistent with the values associated with said first and second blocks *(note column 18, lines 14-17 – the software or program is received into the external memory via the communication link stated e.g. telephone line or internet; also note Fig. 1 – the mechanism for verifying the embedded machine ID dependent code is shown).*

As per claim 12, which is dependent on claim 8, Candelore et al. teaches the computer-readable medium of claim 8, having stored thereon further computer-readable instructions that randomize said list by adding and deleting records selected at random by a random number-generating module *(note Fig. 1 – shows the hardware and associated software to accomplish these functions; also note the random number generator block).*

As per claim 13, which is dependent on claim 12, Candelore et al. teaches the computer-readable medium of claim 12, having stored thereon further computer-readable instructions that wait a specified period of time before allocating said file *(note*



*Fig. 1 – shows the hardware and associated software to accomplish these functions).*

As per independent claim 14, Candelore et al. teaches a system for generating a machine identification for a computing device comprising a file system that allocates storage blocks, each of the blocks having a block identifier that represents the location of a block in a memory of the computing device, the file system maintaining a list of unused locations in the memory that may be allocated for storage of information, the computing device having a database module that allocates memory in which to store database records, that de-allocates records upon request, and that places de-allocated records on the list whereby the de-allocated records may be reallocated for storage of information, the system comprising:

a database creation module that uses said database to allocate a number of dummy records *(note column 23, line 38 – capability for generating and dealing with dummy data between storage and secure device is described; also note column 18, line 62 and Fig. 1 – describes the database functionality; also note claim 14 and 26 in columns 33 and 34);*

a random number generator that selects dummy records to be deleted, wherein the selected records are deleted, and the locations of the deleted records added to the list of unused locations *(note Fig. 1 – refer to the random number generator block which perform this function); and*

a machine identification generator that allocates a file comprising a plurality of blocks allocated from the list of unused locations in the memory and generates a machine identification based on the block identifiers for the blocks of which the file is comprised *(note column 15, lines 42-50, column 19, lines 40-43, and column 24, lines 54-67 – the key is the unique identifier which is dependent on randomly selected memory address; also note Fig. 1 and*

Art Unit: 2132

*column 19, lines 36-43 – a unique machine identifier can be created using the object ID associated with the memory blocks in the storage device).*

As per claim 15, which is dependent on claim 14, Candelore et al. teaches the system of claim 14, further comprising:

a software acquisition module that uploads the machine identification to a server which creates a program based on the machine identification and which stores the program in the file, the machine identification being embedded within the program, the program containing instructions which verify that the machine identification embedded within the program is consistent with the block identifiers of the blocks comprising the file in which the program is stored *(note column 18, lines 14-17 – the mechanism for uploading the machine ID and receiving the program that embeds the machine ID is described; also note Fig. 1 – the mechanism for verifying the embedded machine ID dependent code is shown).*

As per claim 16, which is dependent on claim 15, Candelore et al. teaches the system of claim 15, wherein the machine identification is embedded in the program in an obfuscated form *(note column 16, line 65 – the obfuscation technique is described; also note Fig. 1 – the obfuscation technique can be used to obfuscate a program).*

As per claim 17, which is dependent on claim 14, Candelore et al. teaches the system of claim 14, wherein said machine identification generator generates the machine identification by concatenating the block identifiers for the blocks of which the file is comprised *(note Fig. 1 and column 19, lines 36-43 – a unique machine identifier can be created using the object ID associated with the memory blocks in the storage device).*

***References Cited***

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Rubin et al. (US Patent # 5,680,573) teaches a method of buffering data objects in a database.

Chidambaran et al. (US Patent # 6,253,226) teaches a duration-based memory management of complex objects.

Farber et al. (US Patent # 5,978,791) teaches a data processing system using substantially unique identifiers to identify data items, whereby identical data items have the same identifiers.

***Conclusion***

8. **THIS ACTION IS MADE FINAL.** See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

***Inquiries***


9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shahin Mizan whose telephone number is 571-272-0687 and whose fax number is 571-273-0687. The examiner can normally be reached on M-F 8:30 a.m. - 5:00 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on 571-272-3799. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Shahin Mizan  
Examiner  
Art Unit 2132

SM  
SM

  
GILBERTO BARRON JR.  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100